

Multi Memory Test Chip RTEST100

PREAMBLE

RTEST100 is used to test Presscon Multi Controls (PR/MULTI) Memory IC8 & IC9. PR/Multi supplied from November 2007 to May 2008 may have intermittent memory chips IC8 & IC9 which will exhibit incorrect timing results and other intermittent or incorrect operation. The Barcodes range is PHC13440 to PHC14626 Controllers supplied from May 2008 onwards (labelled ND080519 or later, or later barcodes) will be tested and do not expect the problem.

The RTEST100 Chip can be plugged into an existing controller without changing any settings and quickly check for correct operation for IC8 & IC9.

TEST PROCEDURE

1. Remove power to Control (ie ensure display is blank)

2. Referring to picture below, Unplug existing EPROM in position IC4, Noting the orientation of the notch.

3. With the same notch orientation, plug RTEST100 into socket, ensuring all pins are engaged and none are bent.

** Recheck orientation and pins again before power up **
If IC4 is plugged in reverse
DAMAGE will occur!

4. Power up controller.

5. After a few seconds the display will report a pass fail on IC8,IC9.

6. Assuming a pass, remove RTEST100.

7. Replug original IC4 Memory chip.

** Recheck orientation and pins again before power up **
If IC4 is plugged in reverse DAMAGE will occur!

Controller settings will not be changed and operation can be continued.

If a controller fails on IC8 or IC9 return controller to Phasefale for repair or replacement. Phasefale apologizes for any inconvenience caused.

PN490 Issue 1 23/7/08

